Notic f R ferenc s Cited

	Application/Control No. 09/542,174			Reexamination LIN ET AL.		
	Examiner	Art Unit				
	Matthew C. Doolev	2133	Page 1 of 1			

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-5,402,377	03-1995	Ohhata et al.	365/200
	В	US-5,838,893	11-1998	Douceur, John R.	714/7
	С	US-6,408,401	06-2002	Bhavsar et al.	714/7
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	Н	US-			
•	1	US-			
	J	US-			
	Κ	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	Ø					
	R					
	S					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	R. Torrance et al., "A 33 GB/s 13.4Mb Integrated Graphics Accelerator and Frame Buffer," IEEE International Solid-State Circuits Conference1998;Digest of Technical Papers. 1998 IEEE International, 1998 pp. 340,341,461.			
	V				
	w				
	x				

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.